	Case 1:19-cv-12551-FDS Document 161 Filed 04/06/21 Page 1 of 85	
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1	UNITED STATES DISTRICT COURT DISTRICT OF MASSACHUSETTS	
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4	SINGULAR COMPUTING LLC,)	
5	Plaintiff) Civil Action	
6) No. 19-12551-FDS	
7	VS.)	
8	GOOGLE LLC,) Defendant)	
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10	BEFORE: CHIEF JUDGE F. DENNIS SAYLOR, IV	
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12	MARKMAN HEARING CONDUCTED BY ZOOM	
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15	John Joseph Moakley United States Courthouse	
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1 PROCEEDINGS THE CLERK: Court is now in session in the matter of 2 Singular Computing vs. Google, LLC, Matter Number 19-12551. 3 Participants are reminded that photographing, 4 5 recording or rebroadcasting of this hearing is prohibited and 6 may result in sanctions. Would counsel please identify themselves for the 7 record, starting with the plaintiff. 8 9 MR. HAYES: Paul Hayes for Prince, Lobel. 09:00AM 10 MR. VELLA: Matthew Vella, Prince, Lobel. 11 MR. GANNON: Kevin Gannon, Prince, Lobel. 12 MR. SEEVE: Brian Seeve, Prince, Lobel. 1.3 THE COURT: Good morning, all. 14 ALL PLAINTIFF'S COUNSEL: Good morning. MR. SPEED: Good morning, your Honor, Nathan Speed. 15 16 I'm joined by Matthias Kamber and Michelle Ybarra from Keker, Van Nest and Asim M. Bhansali from Kwun, Bhansali, Lazarus. 17 18 MR. BHANSALI: Good morning, your Honor. 19 MR. KAMBER: Good morning, your Honor. 09:00AM 20 THE COURT: All right. This is the Markman Hearing 21 in this proceeding. I'm not sure what makes sense in terms of who to go first, so I'll go with Singular unless someone 22 thinks that's a bad idea. 23 24 So, Mr. Hayes, the floor is yours. I think I may 25 have told you, I have a proceeding, actually a Zoom call with

the Bar at noon where I've had up to 1200 people on this call 1 in the past. It's an update on COVID. 2 3 That's my absolute drop dead, and my only chance for lunch is to try to grab something between 11:45 and 12:00 4 5 because of the rest of my schedule today, so let's see if we 6 can't get this done in some reasonably prompt time. Having said that, as I think I said last time, don't 7 be worried talking down to me or putting this in baby talk. 8 I will not resent it, I will appreciate it. 9 09:02AM 10 Mr. Hayes or your team, the floor is yours. 11 MR. HAYES: Thank you, Judge. I think that the 12 parties, which we proposed, worked out some schedule, which I 13 have agreed to with the other side where I'll address the 14 proposed construction that we put forth and their first one, 15 and then they're going to be able to address everything and 16 then I'll reply. 17 THE COURT: That's fine. 18 MR. HAYES: Okay, thanks. We might as well start. 19 Do you have the actual slide deck? THE COURT: I don't have it in front of me. 09:02AM 20 21 received it, but I don't have it in front of me. 22 MR. HAYES: We'll put them up on the screen then. 23 All right. Okay. 24 THE COURT: Okay.

MR. HAYES: Here we go. All right. In any event,

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that one doesn't tell us much, if anything. The next slide, please, slide 2. Okay. In any event, Judge, this is the word or the phrase that we're asking for construction is the term "execution unit."

Now, the reason I've given you this entire three lines is to give you some idea the context in which this is used so we're not just taking words here, there, and everywhere.

As you can see from the claimed invention, as Bates claimed it, this execution unit is adapted to execute a first operation on a first input signal, so that's what we're talking about when we're talking about, "an execution unit."

The next slide. Now, our proposal is that the execution unit be construed to mean "a processing element that's comprising a memory circuit paired to an arithmetic circuit." And "paired" means connected, obviously.

Now, and Google's proposed construction basically is not -- I mean, they disagree here, there, everywhere, but there's not an affirmative proposed construction, so if we look at it, to go through this pretty quickly, on the next slide, we got it here, Google from the briefing has conceded that the term "execution unit" comprises a processing element, as we suggested, paired to an arithmetic operation, and that's conceded in their briefs, which we cite here for you.

So that leaves the only issue here on my argument is whether to include memory and circuit, as we suggest, so the issue has been paired down pretty good for us and for everybody.

Now, the next slide, moving on, on the issue of memory and circuit is this obviously comes from the patent, and so we have to look to the patent to see what, if anything, a processing element is because Google has already conceded that, in fact, the execution unit is a processing element.

So if we look to see, okay, if we know that, what is it, and it says here for the purposes of discussion, we call each unit, which pairs memory with the arithmetic a processing element or PE.

And that's why we ask the Court to, when they construe the term "execution unit" to be a processing element, as Google concedes, that is paired with arithmetic, which Google concedes, but it's also paired with memory, and, clearly, I don't think Google is going to really argue, have the hutzpah, so to speak, to argue that these things aren't circuits.

As you can look at it, that's a circuit as you can see in Fig. 4, and as you can see, the processing element, Fig. 4 is the processing element.

Now, that's something I think you should note

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because later on Google is somehow trying to confuse Fig. 4 and Fig. 6. Fig. 6 is the arithmetic element. The processing element that we're all talking about is Fig. 4, period, and the arithmetic unit, you can see it if you look straight at it, and you see where it says 408. That's the arithmetic unit, not the processing element, and it's clear as a bell in the patents and the briefs and the rest, and I think they agree that's the processing element for now.

Now, as you can see what this is doing in the processing element, you add an arithmetic unit right there in the middle, 408, and that's paired to a register, and a register is a memory. There's no dispute about that, so that's consistent in the intrinsic evidence, and it's consistent, obviously, with what is described and we put in the slide here in the patent, et cetera.

And as for the idea that's a circuit, I mean, our position is you just look at it. Anyone skilled in the art knows we're talking about a circuit here, and if you look further in the patent, it says the physical implementation of the PEA, that's a processing element array, e.g. chip could be replicated, e.g. tiled on a circuit board.

Obviously, you have circuit boards, and you have circuits on chips, and that's what we're talking about here, so that's why we suggest basically that's the way to go.

Now, to briefly go through, first of all, Google

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argues, next thing, that there's no circuit because it says there's no circuit should be construed into the claim because the claim is directed to software embodiments. That affirmative argument by Google is just simply incorrect, at best.

The claim, as you can read, is directed to an execution unit. That's a thing. It's directed to a processing element as construed. That's a thing, it's not a software, and the processing element is described throughout the patent and the claim as having inputs and outputs, signals and the rest, so this thing is not directed to software on any stretch of the imagination, and what is directed to software, paragraphs -- excuse me, claims 33 and 68, which I put in this slide to just show you what happens when you want to claim something directed to software, you say so, and they did, but the difference here is that they're not asserted, so I think it's fairly clear that that argument by Google is not particularly a good one.

Now, their next argument, moving on, is they say that, well, if you want the word "memory" into the claim, then there's no difference between claim 53, which is the claim at issue and claim 25, which is some dependent claim somewhere. Well, the problem with that, that is incorrect on its face.

Claim 25 calls for basically, as you can see, a

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device includes memory locally accessible, so it's talking about a local accessible memory. A local accessible memory is not a memory. Obviously, memory is broader, and thus the notion that the scope of 53 and 25 are the same is just simply the argument factually is incorrect. It's also legally, as we put in the brief, irrelevant anyways, but the fact is what I want to point out is that the underlying factor is incorrect on the claim.

The next one, their next argument they make is they say not all embodiments in the patent have memory paired, have a paired memory. They affirmatively say that. That statement is false. Every embodiment in this patent has a paired memory, period.

Now, what do they do in the brief and everywhere, they say look at Fig. 6, and they say Fig. 6, there's no memory paired to Fig. 6. True. Well, the point is -- well, actually there is, but so what? Fig. 6 is not the execution unit, Fig. 6 is not the processing element, Fig. 6 is the arithmetic unit, and the claim is talking about an execution unit, and that's what we're construing, so that argument just simply isn't there, and, finally, even if it were, which it isn't, that's the log down the bottom that obviously you could have multiple embodiments, and each claim doesn't have to cover every embodiment, so I think legally but, more importantly, factually, the argument is just misstating given

the intrinsic evidence as we see it, and then we have that.

We've gone over our argument, which we believe is based on the intrinsic evidence as to why memory should be there, our argument why circuits should be there, and I've gone over every one of Google's arguments, which I pointed out are factually wrong, given the intrinsic evidence as set forth right on the screen, so that's our argument on the claim construction of execution unit.

Now, pursuant to this, what do you call it, an agreement, we'll go to Google's first claim of how they want to construe something in the claim, and if we look at the term that they want to construe, the language is on the top with the blue background. It says, "Low-Precision High Dynamic Range Execution Unit."

Now, our construction is we say that that construction, there's no needed construction for this other than the term execution unit, which I just went over, and otherwise it's plain and ordinary.

Now, what's interesting is Google's construction in the IPR. They have an IPR, and in the IPR what they represented to the patent office is that this identical phrase need no construction, plain and ordinary.

So they go to the Patent Office and say this construction is fine, it needs no construction, this term

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plain and ordinary, and now they come into the District Court 500 miles north and rewrite the claim, as we will see, which, obviously, they can't do, and I'll show you why.

The next slide is indeed the language where it says, "Low precision high-dynamic range (LPHDR) execution unit," and I've highlighted in yellow all of the sentence so that you take it in context. We can't start taking words here and there and pick them out of context, and the execution unit, what is this low-precision high-dynamic execution unit? What does it do? What it does, it says it's adapted to execute a first operation on a first input signal. That's exactly how it's claimed, and that's the context of the phrase that we're going to talk about right now.

So what do we have here? From a legal point of view, it's obviously bedrock principle of construction, as they've got here, the construction begins with the language of the claim. And the claims, and it's also bedrock black letter law the courts do not rewrite claims. Instead, you give effect to the terms chosen by the patentee, and as clear as a bell, the claim as written and allowed and described in the patent shows that this execution unit is adapted to execute a first operation on a first input signal, period.

That's exactly how it's claimed, and they cannot just come in here and try to rewrite the claim because they don't like it and try to gin up, which is all that this is,

to gin up some non-infringement argument.

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So we get the next slide, and the next slide is there to basically show you how they are indeed trying to rewrite this claim, and, most importantly, and I'll get to this in a second, but, most importantly, in their attempt to rewrite the claim on this level, what do they do? They delete the term "signal." They get rid of it. Well, you can't just delete the way it's claimed because you don't like it. That's the way it's claimed, and that's it.

You could try to prove it invalid in the sense of obviousness or whatever you want or you could try to prove you don't infringe, but you can't start changing the parameters of the claim, so if we look at that, and this next slide basically says it all. This is exactly under the law what you can't do.

First, they delete LPHDR, which modifies the execution unit, so they get rid of that. For what reason? There is no reason. First of all, you can't do it. They change the word cutely from "designed to" to "adapted to." Then they say it's designed to perform, however, the claim doesn't say it's designed to perform, it says they got adapted to execute, right, so it should say designed to perform, I mean, adapted to perform, and what do they got, "adapted to execute," so they cutely change the word "perform" to "execute," and then they say what? They add in

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"designed to perform arithmetic operations," adding that in,

"to execute a first operation," which is exactly not what the

claim says.

And, lastly, in rewriting the claim, it says

"designed to perform arithmetic operations on numerical

values," right? Well, the claim is talking about signals

specifically, and as you can see here, in no uncertain terms,

what they have done is changed like 80 percent of the words

of this phrase to take it all out of context, and then they

will gin up some type of non-infringement, and what they're

trying to do is when they say, "final result, designed to

perform arithmetic operations on numerical values," et

cetera, that's not what the processing element does.

The processing element processes a signal as specifically claimed, and, most importantly, on this issue, Judge, most importantly on this issue, and I think this is the key, this is the same claim that Google represented to the Patent Office, the PTAB, needs absolutely no construction, plain and ordinary, and now as you can see from this list of changes by Google, what they're trying to do is rewrite the claim. This is rewriting the claim that they presently represented needs no construction.

I think what they're doing, they should not be allowed as a matter of law to do any of this because it just doesn't make any sense whatsoever.

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So, the next one is moving onto with that, we can see from the intrinsic evidence that, in fact, the claim is directed to the execution unit processing signals. The input is a signal, and that's exactly how it's described in the patent. It says on the top right, the input and intermediate values received by output, by and operated on by the PE400. The PE400 is the processing element, made, for example, take the form of electrical signals representing numerical values.

That's precisely how it's claimed. It's claimed in terms of an input being an electrical signal, an output, being an electrical signal, each representing a numerical value, but the input and output are signals, period, and so that's, obviously, I point that out because the claim is obviously consistent, exactly, almost, with the prosecution, with the specification, as you can see.

Now, the next one, it's not a tutorial, but to me, it's almost self-apparent, but the claimed invention is an electronic device. That's what it is. We know that, and it operates on input and output electronic signals because it's trying to process, for example, artificial intelligence, a signal representing an artificial intelligence issue comes in, and it comes in as an input signal, and it goes out as an electronic output signal.

To change that is, I think, a ludicy, but, in any event, numerical values, they don't travel on wires. They

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don't. They just hang in the air and travel around the circuit.

What travels around a circuit and what an input to a circuit is is a signal. That's how it works, and we have the unrebutted testimony of our expert, and particularly pointing out the difference between signals and values, and, most importantly, indicating that they're not necessarily the same, all contrary to the arguments by Google, to begin with, and that is unrebutted because notably their expert says nothing about signals, values or anything.

They can have lawyers, have lawyer arguments to you until the cows come home, however, this is actually unrebutted evidence, and it is what it is, Judge, so, I mean...

And the next thing is they make an argument in their brief, and this was in the original brief, saying an input signal cannot be high-dynamic range, because the claims calls for the input signal to be a high-dynamic range, and they say, well, that can't be.

This is, again, the unrebutted testimony of our expert indicating that, indeed, signals can be high-dynamic range, exactly opposite, so we have lawyer argument saying they can't, and then we have actual unrebutted evidence saying they can, and, to boot, if you talk about that, you talk about extrinsic evidence, their expert did none of this.

The bottom line is now I believe this is true, and Google can respond to it, but from my reading of the briefs, they now finally concede that an input signal can be of high-dynamic range, and that's the way I read their brief to be, if I'm reading it incorrectly, I don't think I'm reading it in incorrectly, and I'm not in the business of cropping quotes or anything to that effect.

But irrespective of what they argue, they argue it's all attorney argument because this is unrebutted expert testimony about what a high dynamic range can be, can a signal be then? Answer, yes.

So, all that said and done, I would ask the Court to just simply find given what we presented here today that on Google's proposed construction that it be denied and that the Court just find that it's plain and ordinary, which is precisely what they represented to the Patent Office.

We're not asking for any more than what they told the United States Patent Office probably a few months ago, so that's our take on this, Judge, on our position with respect to what an execution unit is and memory, et cetera, and their first proposal.

THE COURT: All right.

 $$\operatorname{MR}.$$ HAYES: I think what we agreed to now, you know, they get to do what they do.

THE COURT: Okay.

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1 MR. HAYES: Thanks, Judge.

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THE COURT: Thank you. Who is going to take the lead for Google?

MR. KAMBER: Your Honor, Matthias Kamber for Google.

I'll start with a bit of a tutorial, and then Ms. Ybarra will handle the term "repeated execution" and then Mr. Bhasali will address the LPHDR execution unit term we just talked about and the first input signal representing the first numerical value phrase as well.

THE COURT: All right.

MR. KAMBER: Let me just queue up the slides here. Hopefully everybody can see those. Your Honor, for getting ready for today, I imagine you probably realized the claim language is rather dense to get through, and we hoped to be able to address that a little bit by providing context for two particular parts of the claim, in particular, this "low precision high-dynamic range," what do those terms really mean, what is low-precision, what is high-dynamic range? They're clearly sort of being compared to something with their comparative language, "low and high," and we want to provide a little bit of background on that, and then the other issue is signals representing numerical values, and specifically we'd like to talk about how computers use signals, how computers use values, and what the distinction is between signals and values as they are used in a computer

chip.

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So let me take the first of those. This issue about precision and range and start maybe not at the level of baby talk but at the level of just basic numbers and how numbers are used, and probably the most basic form that you're familiar with is just an integer.

That's a whole number, no fractional component, that is sort of no decimal point, and these numbers, examples are zero or 26 or negative 2065, and numbers like this can be used to express something, but they only have so much precision if you're trying to express something that's in between 26 and 27. You can't do it with an integer, you kind of have to round to the nearest whole number.

For that reason, there's something known as a fixed point number, and those are used, those have some degree of precision. They are real numbers that have some fractional component where there's a fixed number of digits after the decimal point, and the example that we have here has two, has that fixed point in a place where it allows for two decimals in the fraction part of the number, 123.45, so 45-hundredths there is the fractional part, and this allows you to express numbers with some greater degree of precision because you're not limited to integers.

And, finally, there is what's known as floating point number format, and this is something you may or may not

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remember from the 101 briefing. It's similar to scientific notation. It allows you -- it's a real number that has a fractional component and a varying number of digits after the decimal point or after the -- yes, after the decimal point, and so this allows you to express numbers with a greater degree of range because you're multiplying it by 10 to some power, 10 to the second power would be 100 here.

And in the examples here, we also have 10 to the 5th, so 10 to the 5th would be 10,000s, and the number would be expressed in that way.

So these are the three different types of formats, integers, fixed point numbers, floating point numbers, and they kind of set up what I think is the discussion really about precision and range.

Now, here's an example of the distinction. Range is really I think can be simplified to the decimals to the left of the decimal point, and precision is really the fractional part to the right of the decimal point, and here we have an example of a number, 47,582,473 is the integer part of this, and that is the range part.

The range can go all the way up to, you could imagine with this type of number, 99,999,999, so you have an equal number of decimals to the right and left of the decimal place here, you have 8 to the left, you have 8 to the right, and you can express a certain range and a certain amount of

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precision by making that choice between as to where you put the decimal point.

Of course, you could make a different choice, as we show here on slide 6. Here's another example. Same total number of decimals, that is 16, but 4 are, only 4 are used for precision, whereas 12 here are used for range, and, of course, that allows you to express a much larger range of numbers up to I believe 999 trillion in this particular case, but it only lets you get down to a more limited fractional component, so you would say that this number has higher range but lower precision as compared to the one that we saw on the prior slide.

Conversely, you could have the decimal point in a place where there's more precision and less range, so this would be a higher precision compared to slide 5 but lower range. The range would only go up to 99,999 in this particular case. You'd not be in the trillions, but you could express, if you needed to, a number with a greater degree of precision, that is, you could get down to the level of the hundred-billionths in terms of the precision in which you express it.

I was just showing, was using decimals, of course, and computers and decimals are really 0 to 9, right, the numbers, the decimal numbers, but in a computer, numbers are expressed typically a little differently. Things are

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digital, as I'm sure you've heard, and that means it's Os and 1s. They are bits, and they are just Os and 1s, and you have to represent numbers differently with bits, and here's just an example.

I don't think you necessarily need to know the expression for everything, but it does help set up I think the next few slides.

So, for example, the number 10 is expressed in binary notation by 1010. It's not 10, it's just 1010. 6, for example, is expressed as 110 in terms of the actual bits, and so to get to higher numbers, you need more bits, and that's how bits are used to represent numbers that we traditionally think of in the decimal format.

So let me talk about base-2 floating-point numbers.

Because it's just two options, as we just talked about,

0 and 1, you can't really represent numbers in the scientific notation that you might be more familiar with. That's

Base 10, remember, scientific notation is Base 10, but,

instead, in computers, people talk about or think about it as being in Base 2, and that has a format that looks a lot like this.

There is a fractional component to the number.

There is an exponent component to the number, and that 1 at the very beginning is sometimes referred to as a hidden bit.

It's sort of just assumed by the computer that that 1 is

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there. You don't have to express it, and every number is essentially expressed as 1. something times 2 to the something, and I want to complain that in just a minute, but this is the format that a lot of computers, it's relatively standardized that computers use this type of format. It's a little bit confusing, but let me try to go through and explain it with one or two examples.

Let's take the decimal number 8, just the traditional number 8. Now, the way to express that in this Base 2 notation and decimal, that would be 1 times 2 to the third, 2 to the 3rd is 2 times 2 times 2, and that is 8. So, 1 times 8 is a way of expressing that decimal number 8, and the way that you might express that in Base 2 format, Base 2 notation, you take the binary fractional part of that, which would be 0, which corresponds to the binary numbers 0 and the exponent. There's actually something called an offset in these exponents typically. I don't think we really need to get into that, but it's just the explanation for why the binary equivalent for 3 is not like you saw a few slides ago.

Then sometimes there's what's known as a sign bit.

Most number formats or most computers use a sign bit, not all
do, depending on the context, but a bit might be used for the
sign, but you see here again the hidden bit, you see the
fractional component, and you see the exponent.

Here's another example with the decimal number 3.

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Now, in this particular case in order to make the number 3 in a computer with this Base 2 format, you would say, well, 3 is 1.5 times to 2 to the first. That works out to 3. So you figure out what your fractional part is in binary form. The half fractions is actually expressed by .100, so we show that here, and the exponent, again, it would just be 1, but because of the offset, it shows as something slightly different, and the sign bit here would be 0.

So let me pause to say that I don't think you need to internalize this Base 2 format, really it's just background for something else that I think is important and it's relatively easy to understand, and that is there are standardized number formats that are known and used.

This is an example of the IEEE single precision floating point format. This format was standardized back in 1985, and it uses 32 bits to express a number in that Base 2 floating point notion that we just talked about. That means that there's some number of bits that's dedicated to the exponent and some number of bits that are dedicated to expressing the fractional part of that particular number, and then there's a sign bit as well, as we just talked about, so in this example, and really this standard and perhaps most common format, single precision floating point format, there is — there are eight bits of exponent and 23 bits of fraction, one sign bit for a total of 32 bits, and that's

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just used to express the exponent is in effect the range that lets you get how far out you can get the numbers, the exponent, right, is the part like that scientific notation, and it tells you how much range you have and the fraction, of course, as we've been talking about is in effect the degree of precision you get. The more bits you dedicate to the fractional part, the more precision you can have.

THE COURT: And the sign is either negative or positive, is that what that means?

MR. KAMBER: That's correct, your Honor, so I believe 0 is the positive, and 1 is the negative, but I'm not entirely sure, but it's essentially just that one bit is expressing whether it's positive or negative.

So, in about 2008, right around when this patent was applied for, the IEEE had standardized or was standardizing what was known as the half precision floating point format. This is addressed in the patent itself. It's talking about where it talks about graphics processing units, GPUs and a 16 bit format that was being developed by Industrial Light & Magic, this is what the patent is referring to, this half precision floating point format.

Now, this was lower precision, that is, the fraction is 10 bits as compared to the 23 bits that are used for the standard single precision format. It was also lower range.

They used fewer bits to express the exponent, 5 bits instead

of 8 bits, so this is what you might term a lower precision, lower range type of number as compared to the single precision.

There's also, by the way, the double precision format. This is a 64-bit format that has higher range and higher precision than, or, I should say, more range and more precision really. There's 11 bits for the exponent instead of 8 and 52 bits for the fraction instead of 23, so this is what you would call I'd say a high-precision high-dynamic range type number.

And what you'll hear about perhaps later in this case is that the bfloat16 floating point format that's used by Google and others in the industry, and this has an 8-bit exponent, so the same exponent as the IEEE single precision format, but it takes away 16 of the bits from the precision or from the fraction, I should say, and has a 7-bit fraction, so a total of 16 bits, similar to the half precision format, but it trades some exponent bits for some fractions bits as part of the way that it's designed.

And that's really I think the main point to take away from this, your Honor, is that people, designers trade precision for range. This is from a textbook, Patterson and Hennessey, Computer Organization and Design. It's the fifth design, but this statement is in prior editions as well.

It's relatively well-known that there is some

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compromise being made between the size of the fractions and the size of the exponent, and, by the way, that blue text on the slide is actually original. That is in the textbook.

It's called out.

And as this textbook points out, the trade-off is one between precision and range, increasing the size of the fraction enhances the precision of the fraction, while increasing the size of the exponent increases the range of numbers that can be represented, so it's really just a choice. It's a tradeoff that designers are making sometimes in systems between precision and range, how many bits they are using to express the exponent vs. the fraction in terms of the numbers that they are using.

Your Honor, unless you have questions about that, I was going to move onto the signals and values and how those are used in computers.

THE COURT: Go ahead.

MR. KAMBER: So in the customary sort of silicon-based world of computer processors, there are in effect two different types of signals, analog signals and digital signals. Of course, everything at some point really has to go on a wire in the context of these computer chips, but there are known analog computer types, there are known digital computer types, and there are hybrid analog digital systems as well, and I want to just show or illustrate here

an analog signal waveform.

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You see this here. It has different amplitudes, you could think of this as perhaps different voltages that are coming in to being read, and they're being sampled on some particular time interval, whatever it might be. The time intervals are sometimes small, and the amplitude of the voltage can really represent anything.

If you -- let's say this voltage here on the left, on the Y access, 8 volts could represent the number 100, they could represent the number 1,000, they could represent the number 1 million, however, the designer of the system might want it to work.

There is some conversion in effect between the physical, the voltage, and what it represents in terms of a particular value in the system, but let's just take the basic example where it would be 1 to 1, and you would try to be representing just the decimals here, and as the waveform comes in, it is read, and it matches up to the waveform 05185605, so that comes as being read. Those voltages are being read off, and those can represent whatever it might be.

In contrast, digital waveforms really have only two states, sort of what we talked about before, this binary number system. You really are only using a 0 or a 1, on or off, and so this is what people sometimes call a perfect waveform. Of course, it takes some amount of time to go from

O to the 1, so there's a little bit of a slant there, but this is a perfect square wave in this representation, and most of the time, the O isn't an exact O either, but you're essentially trying to show a high and a low state in a digital signal to represent that O and the 1.

Now, digital signal waveforms because of the way that they are formed -- by the way, this gets, as they comes in, of course, it's being read as the series of either 01001101. As it comes in, it's being read as the one state or the other state, the on or the off.

And for that reason, your Honor, those types of signals, digital signals, are a lot less susceptible to disruption, to noise. Imagine a cell phone being nearby causing interference or heat or any number of things. It might throw off the signal slightly, as is illustrated here in slide 21, but it's not going to throw it off in a way that should generally affect the performance of the computer and how that's read.

Something close to a 0 will still be interpreted as a 0, something close to 1 will be interpreted as a 1, and so the noise here, unless it's really extreme, will not impact how those numbers are interpreted by the computer, those digital signals are then interpreted by a computer.

It's not true, however, for analog signals. Analog signals are, again, it's the example of the noise from

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perhaps a cell phone. They are susceptible to this type of noise. Their waves are more affected, and because you're actually reading the amplitudes, the voltage directly, there's a direct correspondence to the value that it's meant to represent. It is more susceptible to noise.

Heat is a form of noise. Of course, processors generate heat. They have to be cooled in some shape or form most of the time in order to operate properly, so here you see an example where the heat changes this waveform significantly, and because of it, readings like what should read as 5 or 8 are impacted in such a way that they are, pardon me, below 5 or below 8 or at different levels than one might expect, and so analog systems are just known to -- and the experts agreed on this, by the way, in this case -- that analog signals are susceptible to noise. Repeated results for that reason are difficult to have because of that noise feature.

And with that, your Honor, unless you have questions, I'd like to turn it over to Ms. Ybarra to talk about the indefiniteness issue with respect to repeated execution.

THE COURT: All right.

MS. YBARRA: Good morning, your Honor,
Michelle Ybarra from Keker, Van Nest & Peters for Google.
We'd like to start our argument with the term, "repeated

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execution" because it's case dispositive, so should the Court find the term to be indefinite, as we think it should, all of the asserted claims are invalid, and here's why we believe you should find repeated execution is indefinite.

Matthias, could we go to the next slide, please.

Repeated execution appears in all of the asserted claims and appears specifically in the claim language that provides the test for determining whether a device infringes.

If we could go to the next slide. We're looking here specifically at claim 1 of the '273 patent, and this is the test. It's kind of dense, but I'm going to drill down on this briefly.

The claims cover an LPHDR unit that after repeated execution of an operation produces results that differ on average by a certain degree from the results of an exact mathematical calculation of that same operation.

If we could go to the next slide. This is kind of isolating the language that's doing the work here, and I think it's helpful to hone in on this. This says, "A device falls within the scope of the claim if the statistical mean of the output values over a repeated execution of an operation differ by at least .05 percent from the exact calculation."

So the claimed invention doesn't evaluate results on an execution by execution basis, but it's looking at the

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average of results after repeated execution of an operation and compares that average to the results of an exact mathematical calculation. In that way, infringement is defined primarily not by how the device operates but by the results it produces, and this test has particular consequences for analog systems, which are susceptible to noise that can cause the system to generate different results for the same inputs, as Mr. Kamber was just explaining.

In an analog embodiment, noise will cause the results to vary, therefore, the number of times an operation is repeated will shift the average of the statistical mean with each execution of the operation.

If we could go to the next slide. And that therein lies the problem. The claim language that supplies the test doesn't answer the fundamental question how many times must one repeat the operation in order to average the output and compare the results to an exact mathematical calculation, so the claim language creates a zone of uncertainty about the scope of the invention and the circumstances under which a device infringes.

Nautilus makes clear that the claims when read in the light of the specification and the prosecution history have to provide reasonable certainty for those skilled in the art about the scope of the invention, so it's not sufficient that the claims capable of interpretation, as we believe

Singular suggests.

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There's no doubt that repeated execution here means more than one, but that tells you nothing about how many executions are necessary such that you can calculate the statistical average of the output values and compare that average to the result of an exact mathematical calculation to figure out whether those figures differ by the minimum amount claimed in the patents, and it's the basis of that comparison that tells you whether a device infringes, but there's nothing in the claims, the specification or the prosecution history that provides any kind of objective anchor that makes the term definite.

If we could go to the next slide. As I mentioned before, this is particularly consequential in the context of an analog embodiment where the same testing can lead to different results.

Google's expert, Dr. Wei, demonstrated when noise is a factor, as it is in analog embodiments, performing the same operation with the same inputs will yield different results, the average of which may fall above or below the claimed error threshold described in the patents.

And Singular's expert, Dr. Khatri, agrees. He says in paragraph 33 of his declaration devices that using analog signals to represent numbers introduce noise in their computations and performing the same operation twice with

identical inputs will statistically produce different output values and initially a fluctuating arithmetic average.

In his own experiment reflected in the graph at paragraph 34 of his declaration, he describes the arithmetic average as unstable and one that fluctuates significantly over short periods of time, so what that means is that the average will potentially drift above and below the claimed error threshold and will sometimes satisfy the claim language, the test in the claimed language and sometimes won't.

THE COURT: Let me just make sure I understand. So we're talking only -- let me put aside the analog hybrid, whatever that is, so we're talking about analog signals and we're talking about a short period of time, correct?

MS. YBARRA: Yes.

THE COURT: In other words, over time, this, I don't know what the right word is, but flattens out or this noise business becomes less important, in other words, if you perform this 10 million times, it's different from 10 times?

MS. YBARRA: That's what Dr. Khatri contends, and that's what his graph shows.

THE COURT: Okay.

MS. YBARRA: Exactly what is a short period of time, he does not define or specify.

THE COURT: Okay.

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MS. YBARRA: If we could go to slide 29. I'm sorry, we are on slide 29. Slide 30 then. So, as I was saying, how many times one performs repeated executions of this same operation is critical to whether or not the test is satisfied, but there's nothing in the spec. or the prosecution history that provides guidance on how many repeated executions are necessary.

That's not just Google's position but Singular itself fails to cite any intrinsic evidence on this point.

Instead, Singular relies entirely on the opinion of his expert, Dr. Khatri, who offers nothing more than an opinion based on his subjective experience.

And here's how Singular frames it on slide 30:
Singular says you can't look at repeated execution in
isolation because the whole point of performing repeated
executions of an operation is to obtain the statistical mean
of the output values.

And, again, relying entirely on Dr. Khatri's opinion without citing any support in the spec., Dr. Khatri opines that a person of skill in the art would rely on the law of large numbers to understand the scope of the claim, and law of large numbers states that the average of the results obtained from a large number of trials should be close to the true or expected value and will tend to get closer to that value the more trials you perform, so this is the line

flattening out that you just referenced, your Honor.

Dr. Khatri argues that a person of skill in the art would know the law of large numbers, and, therefore, would understand that executing an operation enough times will yield a stapled population mean, where the average won't meaningfully fluctuate, and you can determine whether invention satisfies the claims. What if no staple mean ever emerges? Dr. Khatri argues then the repeated execution limitation is never met.

And if we could break this down because none of this is derived from the specification of the prosecution history. These are entirely Dr. Khatri's opinions, and he concedes that how many repeated operations or repeated executions of the operation are necessary to satisfy the claim language is a completely subjective standard.

So going to slide 31, first, as to the law of large numbers, which Dr. Khatri contends a person of skill in the art would apply when interpreting the claim language, he can't point to anything in the claims or the specification or the prosecution history that references the law of large numbers, and he, in fact, rejects the notion that the skilled artisan would know the patent incorporates the law of large numbers, saying I don't know where you got that idea from, I never said that.

If you look at his declaration, paragraphs 27 to 36

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of his declaration, Dr. Khatri fails to cite any intrinsic evidence on the number of required executions, so he can't identify any guidance as to the number of repeated operations necessary to satisfy the claim language.

If we could go to the next slide, please. Instead, Dr. Khatri says you perform enough repeated executions until the statistical mean is stable, and how many executions are required to reach a stable statistical mean? Well, remember, Dr. Khatri admits that in an analog system, performing the same operation will produce different output values, and initially a fluctuating average due to noise.

And I want to make clear in reference to the question you asked earlier, your Honor, it's not the noise, noise doesn't change or go away over time, it's just that it averages out, the line flattens out, so Dr. Khatri says initially you get a fluctuating average. He doesn't identify the point where the average stops fluctuating, but he says at some point, the average will stabilize, and it's at that point you've performed enough repeated executions that you could compare the new stable average to the results of an exact mathematical calculation and determine whether a device infringes the asserted claims, and the problem with Dr. Khatri's interpretation, and he concedes this, is that what he deems as stable a statistical average or what he deems is a stable statistical mean is entirely subjective.

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He describes a stable statistical mean as an average that never materially changes again and does not meaningfully fluctuate or has ceased to fluctuate significantly. All of those determinations, whether the average doesn't materially change again or meaningfully fluctuate or ceases to fluctuate significantly are subjective determinations that according to Dr. Khatri are context dependent.

If we could go to the next slide, please. At his deposition, Dr. Khatri was asked multiple times how a person of skill in the art would know when an average is stabilized, and his answer was always some version of what you see on slide 33.

For example, he says a person of skill in the art familiar with a particular circuit, application and technology would know when an average doesn't meaningfully fluctuate, and they would know for certain application what meaningful is in terms of meaningfully fluctuating, and a person of skill in the art would know for their circuit when that happens, when that was reached.

This is precisely what the Federal Circuit says you can't do. In the *Datamize* case that we cite in our supplemental claim construction brief and the opening brief, the Federal Circuit says, "The scope of the claim language cannot depend solely on the unrestrained subjective opinion of a particular individual purportedly practicing the

invention."

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Nowhere does Dr. Khatri offer any objective indicia by which a person of skill in the art would make this determination that's sufficient repeated executions have been performed to calculate those statistical mean. It's always context dependent, according to him.

If we could go to the next slide, please.

Dr. Khatri even gave this example about a circuit that is designed to operate on Mars. For that circuit, he said,

"What is a meaningful variation in the average of the outputs that would differ from a circuit operating on Mars, between a circuit operating on Mars and a circuit operating on earth?"

In the Mars circuit, the meaningful variations would be much smaller as compared to the earth circuit, where the meaningful variations might be just larger.

So, his position on how many repeated executions are necessary to achieve a stable statistical mean is basically, it depends. It's exactly -- this is exactly counter to what we are taught in Nautilus, the Supreme Courts says in Nautilus, and his answer doesn't provide any objective guidance to a person of skill in the art about the scope of the invention, as the patent statute requires.

There's an additional problem with Dr. Khatri's position. This is we're talking about an apparatus claim here, which either infringes or doesn't but is not

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contextually dependent, so because Dr. Khatri's opinion is based solely on his experience and is not tethered to any supporting evidence in the intrinsic evidentiary record, it does nothing to answer the question that we're still left with, which is how many repeated executions of an operation are necessary and the context of the asserted claims.

So the metes and the bounds of the claims are not clear, and the claim is indefinite. If we can go to the next slide, please.

Singular alternatively argues that this is not a dispute that the Court can resolve now because there are disputes of fact that preclude deciding this issue at claim construction, and it should go to the jury, but there are no disputes of fact here, and we elaborate on this on pages 1 to 3 of our supplemental claim construction brief, and we distill those points here on slide 35.

There's no dispute that repeated executions applies the test, no dispute that the spec. and the prosecution history fail to provide guidance about how many executions are required, and neither Singular nor Dr. Khatri can point to any intrinsic evidence on the number of repeated executions required or discussing the law of large numbers or in support of Dr. Khatri's theory that enough repeated executions will produce a stable statistical mean that a person of skill in the art will know when they see.

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There's also no dispute the asserted patents cover analog systems that will produce inconsistent results each time you execute an operation, meaning the average of those outputs will shift depending on the number of executions performed, leaving you with a mean that can drift above or below the claimed error threshold.

The only disputes between the parties are over the legal conclusions drawn from these facts, but it's not about the facts themselves, and the dispute over the legal conclusion drawing these facts is a question for the Court that the Court can and should decide now because Google has met its burden with clear and convincing evidence to show repeated execution is indefinite. We believe the Court should so find. I'll stop there. If you have any questions, your Honor.

THE COURT: No. Okay.

MR. BHANSALI: Good morning, your Honor.

Asim Bhansali of Kwun, Bhansali, Lazarus, and for Google, I'm going to address the two remaining terms, and I'll start with "low precision high-dynamic range execution unit" term, and if we could go to the next slide.

So before talking about the intrinsic evidence, your Honor, I just want to address this point that Mr. Hayes mentioned a few times about Google's position on this term in the Patent Office in the IPR that we're waiting for the

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institution decision that we're going to get in the next month and a half or so.

There's nothing, he hasn't pointed to any inconsistency between the position we've taken there and what we're saying here. It's not at all unusual for a petitioner not to seek a construction in the PTAB where the petitioner doesn't believe that that construction is necessary to map the prior art onto the claims.

So it's not really that we're taking at all an inconsistent position, they haven't pointed to any inconsistency, it's just the construction in our view wasn't necessary at the petition stage to show that the prior art maps to the claims.

And the other thing I would say, your Honor, is we also heard a lot from Mr. Hayes about how Google is purportedly rewriting the claim language, but here we put, to start on this term, our proposed construction on the right and Singular's on the left.

And as you can see, and as we'll see in a moment when we look at the claim language, our proposed construction is actually much more faithful to the claim language than Singular's is. Low-precision high-dynamic range comes right out of the claim language.

The processing element, we both agree, this is a processing element. The performance of arithmetic operations

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is something we also both agree that the unit does and is also in the claim language and the use of numeric values, being what the arithmetic operations are performed on, as we'll see is very much supported by the spec. and is also referenced in the claim language.

By contrast, it's Singular that is seeking to read a hardware limitation into these claims, and so with that, let me just briefly indicate what the three, what I think the three areas of dispute are here, and then I'll kind of address those in turn.

The first is, as I said, whether or not the claim should be construed to, as the claim language requires, include this low-precision and high-dynamic range as part of the execution unit.

The second dispute is whether or not, as Singular proposes, this hardware limitation, the arithmetic circuit and the memory circuit should be read into the claim.

And the third dispute is whether or not this claim, this term should be construed such that arithmetic operations are performed on numeric values as opposed to on the physical signal.

That last point actually comes up also in the context of the next term, and so just for kind of efficiency, I'll address that in the context of the next term because both, in both terms, the question is whether the arithmetic

operation is performed on numeric values.

So let me start with the first issue, and if we could go to the next slide. The question is whether the execution unit should be construed, whether the term to be construed should be the "low-precision high-dynamic range execution unit," and we would posit, your Honor, that our position comes straight out of the claim.

If we look at the first line of the claim, what's being claimed is a "low-precision high-dynamic range execution unit." Singular would propose to read that out of the claim and simply have an "execution unit" rather than an LP --

THE COURT: I'm not sure, to me, those are adjectives that modify execution unit, so if you put it back in, if you define execution unit to be low-precision high-dynamic range execution unit, isn't it then redundant, wouldn't that claim read, "At least one first low-precision high-dynamic range, low-precision high dynamic range execution unit"?

MR. BHANSALI: If you were to do it that way, your Honor, but we're not -- we're not proposing, to be clear, we're not proposing that because I guess there's a difference between the term we're proposing to be construed and what Singular is proposing to be construed.

The term we're proposing is "LPHDR execution unit,"

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right, so that redundancy that your Honor is positing wouldn't be there, and I think this is actually an important point because the reason why we're proposing to construe LPHDR execution unit is twofold.

First, if we go to the last element that we see on this slide, it says, "Wherein the number of LPHDR execution units in the device exceeds by at least 100," and then it has further language of describing that, so the fact-finder is going have to -- is going to have to find that there are 100 of these LPHDR units, so in the first element, they have to map that and say, okay, what's the LPHDR execution unit, and then at the end, they have to say, okay, there are 100 of these, and so that's why it's important that the whole phrase be defined so that when applying the claim, one can be sure that what we're mapping the first part of the claim on is also where we're finding 100 of these LPHDR execution units.

The only thing, the other point, your Honor, is that Singular's response is, well, we don't need to construe this to include low-precision high-dynamic range because the second clause, that wherein the dynamic range clause defines the low-precision and the high-dynamic range, and we don't necessarily disagree that this defines the dynamic range and an error level, but the point is the claim is on not just an execution unit that executes this way, it's on a low-precision high-dynamic range execution unit, and if we

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look at the other claims in the patent, the other claims in the patent actually have different -- there are other claims in the patent that have different dynamic ranges and different error levels.

In fact, other dependent claims that depend from the same independent claim in the '273 patent that claim 53 depends from, such as claims 47, 48 and 49, have a different dynamic range and a different error level, but, importantly, they all claim the same low-precision high-dynamic range execution unit, so that low-precision high-dynamic range execution unit broadly covers all of those dynamic ranges and error levels, and that's why, that's the second reason why it's important that we're construing this entire concept of the LPHDR execution unit rather than just execution unit, which is actually, you know, a term that is just, you know, just like a processing element.

THE COURT: So 53 is a dependent claim?

MR. BHANSALI: Yes, your Honor.

THE COURT: It depends from what claim?

MR. BHANSALI: It depends from claim 36. It actually has like a sub-dependency, so let me just make sure I state this exactly correctly, your Honor. I have the patent up here. If I could just have one second to scroll to it.

So, 53 depends from 43, your Honor, and 43 then

depends from 36, and so 36 has kind of like most of the 1 language except that it has a different dynamic range and 2 3 doesn't include that part at the end about the 100 units. 4 THE COURT: Okay. MR. BHANSALI: Then 43 adds the 100 units, and then 5 53 changes the dynamic from 1 over 65,000 to 65,000 to what 6 you see here, which is 1 over 1,000,000 to 1,000,000. 7 THE COURT: Okay. Can I ask what I'm sure is a stupid and irrelevant question, but I'm going to ask it 9 10:19AM 10 anyway. It says, "Exceeds by at least 100 to non-negative integer number of execution units." How could that number be 11 12 below zero? In other words, why does it have to say 13 nonnegative? I'm not sure I follow that. If you don't know 14 why --15 MR. BHANSALI: Of course, Dr. Bates wrote the claim, 16 so I can't speak to that. 17 THE COURT: I mean, we're talking about a physical 18 thing, right? Are we not? "Non-negative integer number of 19 execution units." 10:19AM 20 MR. BHANSALI: Well, your Honor, that gets to the 21 next point. The execution unit can be implemented in 22 software, but it is, I mean, even software has an embodiment, 23 right, it does exist, and so the way we read this claim is

that what this is sort of claiming is this idea of the

parallel processing and that there are 100 of the LPHDR

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execution units.

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The non-integer part, as I think your Honor sees, that's the number of units that are adapted to execute -- is the operation that are at least 32 bits wide.

THE COURT: Okay. It's an idle and curious point.

I didn't mean to interrupt you. Go ahead.

MR. BHANSALI: I completely understand, your Honor. That's actually a good segue though, as I said, to the next point, which is whether we should be reading this hardware limitation of the circuit into the claim, and so there, as your Honor heard, they want to read both an arithmetic circuit and a memory circuit into the claim, so let me start with that in general, just the idea of reading the hardware in the claim.

There's nothing in the claim language here that supports reading a hardware requirement into the execution unit. On that basis alone, the attempt to narrow the claim to a hardware embodiment should be rejected.

Now, Mr. Hayes said, well, it's a thing, an execution unit is a thing and suggesting that that is a limitation to a hardware embodiment, but as the specification states and as Singular concedes in the briefing, the specification discloses a software embodiment, which means the claims should be construed to cover that software embodiment, and Singular has offered no basis for why any

particular claim should be construed in a way that reads out that software embodiment.

Moreover, the claim that Singular --

THE COURT: Does the software embodiment have an execution unit?

MR. BHANSALI: Yes, your Honor.

THE COURT: Okay.

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MR. BHANSALI: Yes, your Honor. Well, of the LPHDR execution unit. It is an execution unit, but, again, it's the execution unit that's claimed in the patent, not just a generic execution unit.

And if we could go to the next slide, please. So, Singular, and we saw this same claim actually reproduced in Mr. Hayes' slide, Singular says, oh, no, wait a minute, we have other claims, unasserted claims, that claim the software embodiment so it's okay to read the software embodiment out of the asserted claims.

Well, the problem with that is actually these other claims support our view that you can't read the software embodiment out of the term we're seeking to construe, which is the LPHDR execution unit, so claim 68 is the claim that they cite for having the software embodiment, and, in fact, if you look at the preamble, it refers to a computer readable memory storing computer program instructions, also could be described as software, and then where it says it's emulating

this device.

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Now, what's important here is that this claim, which is the one they identify as the so-called software claim, claims the exact same low-precision high-dynamic range execution unit as in the asserted claim.

If we could quickly go back to the last slide. So here we see highlighted in the first, "Comprising at least one first low-precision high-dynamic range execution unit," okay. If we go to the next slide, back. Again, "One first low-precision high-dynamic range execution unit," so the point is whether it's the claim that Singular itself is saying is the software claim or the asserted claim, the LPHDR execution unit, we'll take Singular's term, the "execution unit" is exactly the same, so you can't construe it in one claim to have a hardware limitation and then say at another claim, it's only embodied in software.

The claim has to be construed consistently across the patent, and, in fact, the family of patents, and so this claim, the very fact that it has a software embodiment of the same LPHDR execution unit that's in the asserted claim supports and, in fact, requires that the LPHDR execution unit in the asserted claim be broad enough to cover software.

So, for that reason, the hardware limitation shouldn't be included at all.

THE COURT: Let me make sure I understand. But

they're saying they're emulating a second device comprising this execution unit, so isn't that different from saying there is going to be a second device, right? I want to make sure I understand this argument because --

MR. BHANSALI: Yes, your Honor.

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THE COURT: -- what I think I heard you say is you can't construe 53 to have a hardware limitation because then 68 doesn't make sense.

MR. BHANSALI: Yes, your Honor.

THE COURT: But isn't 68 saying it's a software that emulates a hardware device, I'm being very simplistic here, but isn't that what 68 says?

MR. BHANSALI: Yes, your Honor, and so that's the software embodiment. In other words, it doesn't -- to be clear, it doesn't say software that emulates a hardware device, it says software that emulates the low-precision high-dynamic range execution unit, and so in this claim, emulation is the way that the software embodiment of the low-precision high-dynamic range execution unit is implemented, and so in order for that embodiment to work, the LPHDR execution unit has to be broad enough to cover software.

So, I mean, I think the emulator is just -- that's just a way that the software is -- because it's not like a software application, right, it's software that's performing

this execution.

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THE COURT: And, again, we're probably spending too much time on this point, but let's say that claim 53 covers an abacus and it's a physical thing, and then claim 68 says software that emulates an abacus. That doesn't necessarily mean an abacus isn't a physical thing, it means you are claiming an abacus and you are claiming software that emulates an abacus, right? I don't see how those are inconsistent.

MR. BHANSALI: Yes, your Honor. If it had claimed an abacus, I think that would be the case, but I think the difference here is they are just claiming the idea of a low-precision high-dynamic range execution unit, and an execution unit is not inherently hardware, it's just, I mean, software is often referred to as having units, right, so, in other words, this isn't a scenario where their term is itself on its face limited to a physical device. It's actually something that could be implemented in software, and they have claims that are doing so, and the specification also refers to having the execution unit embodied in software.

THE COURT: Okay.

MR. BHANSALI: Your Honor, if I could just read from the spec., just, for example, the language here, they have, "Moreover, generally, any of the techniques described above may be implemented, for example, in hardware, software

tangibly stored on a computer readable medium, firmware or any combination thereof."

So, they're making clear in the specification that these techniques, the LPHDR execution can be implemented in hardware or software. Just to kind of reaffirm the point of why this is different from kind of an abacus, because they are referring to an execution unit that could be a software unit.

THE COURT: Okay.

(A recess was taken.)

THE COURT: Are we ready to resume?

MR. BHANSALI: Your Honor, I'm ready.

THE COURT: Mr. Bhansali, go ahead.

MR. BHANSALI: So where we left off was we talked about just the problem generally with limiting the term "LPHDR execution unit" to a "hardware embodiment," but there's a further problem with Singular's attempt to read in this idea of a paired memory into that term, and to be clear, what Singular is asking the Court to do is to read the term "execution unit" as a person of ordinary skill in the art would understand it to require a paired memory circuit.

They are not saying that this is a means plus function claim where we're limited to a particular structure disclosed in the patent. That's not what they're arguing, what they're arguing is "execution unit," just as that term

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generally would be construed has to include a paired memory circuit.

Well, not only is that not supported by the extrinsic evidence of how a person of ordinary skill in the art would understand processing element, as I'll show in a moment, it's also not supported by the specification.

Here, we have language from the specification that talks about the embodiment where the embodiments, plural, where they are including memory as part of their model, and we see here, for example, they say our model of machine, and then they reference it providing a small amount of memory local to each arithmetic unit, and then further down below, if we could stay on that slide for a minute, the prior slide.

Then further down at the bottom, they talk about, they say of which signal is only an instance, and then for purposes of discussion below, we call each unit which pairs memory with arithmetic a processing element, but, importantly, in this entire discussion, they have the language, which is sort of consistent with what a person of ordinary skill in the art would understand an execution unit to be that these are not limitations of the present invention.

In other words, the description in the specification, the one place that they are pointing to where it says that memory is paired with arithmetic, that's part of

a description of an embodiment that the inventor himself, Dr. Bates, said that's not a limitation of the invention, so at the time they wrote the patent, they said this is not a limitation of the invention, but now they're coming into court and saying, oh, well, wait a minute, the LPHDR execution unit has to be paired with a memory.

But that's not simply not the case, not only because they say that here in the spec., but if we could go to slide 43, so slide 43 is -- your Honor mentioned reading a computer science treatise at the break.

This is actually a computer science book that we cited in our papers from the time, actually, prior to the time of the patent, referring to DSP integrated circuits, and it defines this notion of a processing element, and it kind of describes the different elements, and it says this is what a processing element is, and then it says, "We use the more general term "processor" with its internal memory and control circuitry," so the point is here that extrinsic evidence also confirms what we see in the extrinsic evidence.

You, obviously, look to the intrinsic evidence first, as the Court knows, and then the extrinsic evidence, and the extrinsic evidence says, "which is that a processing element does not have to include memory," and so if we can now go back to slide 41, so this is a description on the left, we see the asserted claims, and on the right, we see

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certain dependent claims that actually are dependent from the independent claims that are asserted, and so the asserted claims do not have a memory requirement. That's not described in those claims. By contrast --

THE COURT: What does the locally accessible part of it mean? I mean, that suggests to me that there's some other kind of memory that's not locally accessible, in other words, that there's a broad of category of memory, a subset of which is memory that's locally accessible. Is that wrong?

MR. BHANSALI: Your Honor, the locally accessible certainly is a description of memory, but the asserted claims don't refer to memory at all, so, in other words, it's not as if the asserted claims say wherein the device includes memory, and then the dependent claims come in and say wherein that memory is locally accessible.

The asserted claims don't refer to memory at all, and this is actually one of the problems with the interpretation that Singular is offering. Well, they say, Well, paired with memory, but they don't actually offer any extrinsic evidence or any language from the spec. that says, well, paired memory is different from locally accessible memory.

For example, paired memory Mr. Hayes said today, well, that just means connected to, but there's actually nothing, there's no language where they are saying, well,

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paired memory is somehow different from locally accessible memory, or if we can go back to the prior slide for a moment, for example, shared memory, so the spec. here actually refers to the possibility that you can have a design that includes shared memory, so you can have shared memory, you can have paired memory, you can have locally accessible memory, which may be paired, it may be shared.

The point, your Honor, is that there's no limitation on an execution unit is not limited to any particular kind of memory, it's just an execution unit. And that's clear both from the extrinsic evidence, the treatise we saw, as well as here in the spec. where it says that that's not a limitation of the invention, and so the fact that there are dependent claims that refer to locally accessible memory just simply confirm that the independent claim has to be broader, and they haven't offered any evidence from the spec. or expert declaration or extrinsic treatise evidence that would suggest that somehow paired memory is broader than the local accessible memory, or, for that matter, any support in the spec. for limiting the execution unit to one that has paired memory because they would be limiting it to, at best, it would be limiting it to a particular embodiment where the specification says that embodiment is not a limitation of the invention.

And with that, your Honor, unless your Honor has

questions on this term, I would move onto the last term that
we'd be arguing.

THE COURT: Okay. Go ahead.

MR. BHANSALT: If we could go to slide 44, just

MR. BHANSALI: If we could go to slide 44, just briefly. So this next term is the "first input signal representing a first numerical value."

MR. HAYES: Your Honor, this is Hayes. To be fair to my Brother here, it appears he's going to run out of time in five minutes. I'm just going to give him a heads-up.

THE COURT: I've been interrupting him. Let's see how quickly you can get this done.

MR. BHANSALI: Your Honor, I'll be fairly prompt, but I, obviously, welcome the opportunity to answer your Honor's questions.

THE COURT: Understood.

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MR. BHANSALI: So, there are two issues, your Honor, with respect to this term. The first issue is, and I think I mentioned that this also applies to the prior term, whether or not the LPHDR execution unit operates on values, numerical values, or whether it operates on signals.

Singular says they're relying on plain, ordinary meaning, but if you look at their briefing, and, in fact, Mr. Hayes' argument, they're suggesting that somehow the arithmetic operation is taking place on a signal, and then the second dispute is whether we should refer here to a

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digital or analog representation to clarify what the signal is referring to.

So, turning to the first dispute, your Honor, this is just crystal clear from the specification that what the claim is referring to in the claim language is it's saying that there are arithmetic operations that are taking place on numerical values, and those numerical values are physically represented by a signal. That's the first input signal representing a first numerical value.

If we could go to the next slide. So, this we have four different citations from the specification here, all of which confirm that the arithmetic operations are being performed on numerical values. It refers to perform arithmetic operations on numerical values. It says that processing elements are designed to perform arithmetic operation on numerical values, the input values operated on by the processing element.

Take the form of electrical signals. This is what we're saying. There are electrical signals, but "the values are what are operated on," and then it says, "implementations may vary in dynamic range of space of the values that they process," but you don't have to just take our word for it, your Honor, Singular's own admissions show that they understand that actually the arithmetic operations take place on numeric values.

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Let's go to the next slide, please. Here's an excerpt from somewhat Singular told the U.S. Patent Office in the parallel IPR proceeding just about a month, a little more than a month ago, February 24th, 2021:

"Moreover, whether the numbers are physically represented -- " physically represented. That's our signal.

"-- using charges, voltages, various forms of spikes or other forms or a combination of digital and analog representations -- " note that that language also follows our claim construction "-- has no bearing on the invention, which operates on the values represented, not their physical representation."

So what Singular is telling the PTAB in this statement, and, remember, under Federal Circuit precedent, this statement that they've made in the IPR is now part of the prosecution history of the patent, which the Court can take into account under the *Phillips* case to interpret the claims, they're saying the invention, the LPHDR execution unit, operates on the values presented, values represented, not on the physical representation, and they go on to say that the digital and analog representations are that physical representation. That's how we're proposing to construe the signal.

So, this statement that they made to the PTAB precisely confirms our proposed claim construction, and if we

could go to the next slide, please.

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Not only that, but Singular admitted in its complaint in this case that the patent claims operation, arithmetic operations on numeric values. These are all parts of the claim that we've cited in our briefing.

Again, it refers to operation on an input numerical value. It refers to manipulating numbers, and, again, in the last bullet, it couldn't be clearer. Dr. Bates' LPHDR processing elements, frequently generate in response to request to perform arithmetic operations on high-dynamic range numbers, so, again, this is in their complaint, they're referring to the patent as requiring operation on numeric values.

Now, Singular, nonetheless, takes the position that we're somehow rewriting the claim and reading signals out of it, but the language that they cite from the spec., again, actually supports our interpretation, which if we could go to the next slide, which doesn't read signals out of the claim but puts them in the proper context which is that the signals are the physical representation, but it's the values that are being operated on, so this was language that Singular cited and Mr. Hayes referenced in his presentation, and it's the part at the end that he referred to, which is that it's saying that values take the form of electrical signals representing numerical values, but it's clear from the

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context of this statement that it's talking about the arithmetic operations, and it's referring to the input, output and intermediate values that are received by, output by, and, importantly, for this claim construction, operated on by the PE, the processing element, so, again, the specification, the language that they're citing is saying that it's the values that are being operated on, and those values are represented by signals.

We're not reading the term signals out of the claim, we're simply saying that the signals are the physical representation, but it's the values that are operated on.

THE COURT: I'm struggling to understand what the point of this is or what you're even fighting about. I mean, I am very far from a person of ordinary skill in the art, but I read this, you know, a value is an abstract thing, the number 5, that's a value, it's abstract.

In a computer, that takes the form of an electrical signal, right, which is whatever it is, 101. I don't know how that's done, electrical charge or something, but that's a signal, and it represents a numerical value, which is 5, and I'm not sure what you're fighting about. I'm not sure what your construction adds to that.

MR. BHANSALI: Yes, your Honor. And I actually think the next slide may help to answer that question. So, the important thing here is that this first input signal

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representing the first numerical value, that ends up being the antecedent to another part of the claim, so if we can show the highlighting here on this slide and the next one as well.

Okay. So we have the first input signal representing a first numerical value, and then there's a dynamic range of the possible valid inputs, so the possible valid inputs, the antecedent basis of that are the inputs, the input signals representing the first numerical value, and this is where the issue comes in because what we're saying is that those inputs have to be numerical values.

They can't be signals, and the reason for that, as we'll show on the next slide, is because the possible valid inputs, they're expressed in terms of a dynamic range of numbers, not a dynamic range of signals. And so for the claim to make any sense and for the fact finder to be able to determine whether or not the dynamic range is met and ultimately how the error is calculated, what we have to be looking at are numerical values that are being input to the arithmetic operation, not the physical signals that those numerical values are represented by.

So, if I could go to the next slide. So just a quick reminder of Mr. Kamber's tutorial, and your Honor said this a moment ago, so maybe I'm repeating it, so you've got a numerical value, and then that's represented by a physical

input signal.

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So, if we posit a scenario where, as Singular seems to be suggesting, the arithmetic operation itself is being performed on an input signal, and if we could follow the bill there, we would basically be saying the dynamic range of the possible valid input signals to the first operation is at least as wide as 1 over 1,000,000 to 1,000,000.

Well, that doesn't make any sense because you can't express an input signal that way by describing it as 1,000,000 to 1,000,000. By contrast, you can express a numerical value that way, and so if you take our construction, which is the dynamic range of the possible valid numerical values to the first operation is at least as wide as from 1 over 1,000,000 to 1,000,000, then that part of the claim makes sense.

So, your Honor, to answer your question as to why there's a fight here, it's because we think it's very important to understand that these inputs to the arithmetic operation are numerical values, they are not the physical representation because this part of the claim only makes sense if those inputs are, in fact, numeric values.

Now, if we could go to the next slide. And so they pointed to their expert's position about signals and values, but I want to just make one point here on the experts. Our expert was only opining on indefiniteness, and, notably, they

did not put in any expert declaration in the opening brief on this term. They only put in an expert declaration in the responsive brief, and so we did not then have a subsequent expert response to that, but I think the important point here is if you look at their expert's deposition testimony, he admits that 1 over 1,000,000 to a million is not something that he is saying is the input range of an input signal.

Again, that's just confirming that the way that dynamic range is described is actually the way you describe numeric values, not the way that you would describe the dynamic range of a signal.

If we could go to the next slide, please. This is a longer answer after he says, you know, that's not what he's saying, he kind of is saying a point that Google made about dynamic range, but he's making it clear that that point that he's refuting is actually not saying anything about the patent.

In other words, he's not in any way disputing that the claim language in the patent is expressing ranges in a way that relates to numerical values rather than signals.

And that really gets to one of the -- I think the key point here, your Honor. We're not disputing that numerical values in the claim are represented by signals.

There are --

(Court reporter couldn't hear)

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THE COURT: I can hear you fine. Mr. Bhansali, why don't you back up a bit.

MR. BHANSALI: Yes, your Honor. I was just saying that we're not saying that signals are not part of this claim. In fact, we agree that signals are part of the claim, but the signals are used to represent referred to in the claim are, as the specification values, they're not the physical signals. That's not only what the specification says, but, as I said, is also what Singular told the patent office.

So, lastly, unless your Honor has more questions on this issue, I just want to turn to the last part of this construction, which is the language where we say that signals should be construed consistent actually with the position that Singular has taken as the digital and/or analog representation. That's the other part of this claim term.

And there, your Honor, all that we're doing with our construction is, again, clarifying the language that the signal is referring to the representation of the values, and in this instance, these are a digital and/or analog representation.

That's consistent with the specification. We have two places from the specification cited here that refer to implementation in digital or analog. There's some others that we cited in our briefing.

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And, furthermore, Singular's only response to this is, well, Google would be improperly limiting the claim to some embodiments, but the problem with that position is they haven't identified any embodiment in the specification or that their expert has identified or that might exist that would be limited, that would be read out by our claim construction.

so, we're not actually excluding any potential embodiments, all we're doing is clarifying the language of the claim in a way that makes clear this very point that we've been talking about, the distinction between the values, which is what the arithmetic operation is performed on, and the signals, which are the physical input in the claim, and I would just say it's pretty ironic for Singular to be saying, to be opposing this part of the construction because they're on the one hand saying we're reading the word "signal" out of the claim, but then they're opposing this part of the construction, which actually sort of describes the signal in a way that's consistent with what the specification says and actually what they told the patent office in the excerpt from the IPR proceeding that we saw.

THE COURT: Okay.

MR. BHANSALI: With that, your Honor, I don't have anything further on these terms.

THE COURT: Let me go back then to Mr. Hayes. If

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you think that you need more time than the time you have remaining, you can say so. One advantage of Zoom as opposed to getting on an airplane from San Francisco, it's not terribly inconvenient for us to continue this hearing. Today doesn't work, but, you know, either later this week or maybe spilling into next week.

MR. HAYES: I think, Judge, I can wrap it up pretty quickly.

THE COURT: All right, Mr. Hayes.

MR. HAYES: To start off, anyways, to start off a little bit where my Brother left off, we've got a lot to go over, but he keeps saying that he's not trying to read out the term, "signal." That is absolutely incorrect. The claim says, "A first operation on a first input signal, representing a first numerical value."

In his construction, if you adopt his construction,

I don't get why they're doing this because the claim doesn't

say a first multiplication on a value, it says a first

operation on an input signal, which means you've got an input

signal coming in and a bunch of things can happen to that

signal before it's multiplied. That's exactly how it's

claimed.

And if you look at, for example, Fig. 4, and Fig. 4 is what the processor is. That's what the PE is and what it does, and they just don't go to the multiplier for

willy-nilly execution. They do other stuff, and that's exactly what he wants to change. He wants to say that there's no input signal, that what it really is, a value. The value skips right to the multiplier, and we don't do that. That's what you're going to hear, but that's not how it's written. I can say this forever, but if this isn't trying to rewrite the claim, nothing is.

And then if we could go to the argument, locally accessible, I don't think I have to spend much time on that. We all know memory and locally accessible memory are different. One is local, one isn't. One could be somewhere that's not even in a room, and one could be right on the chip, whatever, so, I mean, that's not even a "issue" that they argue.

Then they argue this issue about the -- oh, it must cover, what did they say, it must cover a software. It doesn't cover software. This is a device, that's what it says, a device having what? A processing element. That's a thing.

The processing element having what? An input.

That's a thing. The processing element having an output.

That's a thing. And if you look at how you claim software,

you say emulate all of that. Nothing of that nature is in

there, so, I mean, that's just a few things.

And also one thing my Brother says, oh, we did

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nothing inconsistent in the Patent Office. Give me a break.

In the Patent Office, both of these terms are plain and ordinary. "Here, we're going to change the whole thing."

So, in that sense, Judge, they are rewriting. And another thing I'd like to bring up, I forgot the first time, if we look at slide 13 because sometimes this is interesting. If you see that, this is the final result --

THE COURT: Hold on, it's not up yet.

MR. HAYES: This is the final result of what their machinations do. It says, "Designed to perform arithmetic operations," perform, not execute, "arithmetic operations, not on signals on numerical values adapted to execute a first operation on a first input signal." That makes absolutely zero sense to begin with just from English.

No one is going to know even what it means, and that's why you don't rewrite a claim, real simple. That's why I submit it's plain and ordinary.

Now, if we go to his argument, the argument if you pull up again slide 19, this is their second attempt here that my Brother emulated. He's tried to say, oh, we're going to rewrite this term, and this is exactly -- you got the wrong thing. 19. Okay, here we go. Sorry.

Look at what we have here. They add analog to the claim. Analog is not in the claim. They add digital to the claim. They add and/or to the claim. They delete first,

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they delete input signal. That's gone. They delete that the input signal is representative of anything, and they change signal to value.

This, again, is the same claim that they represented, which is an admission, to the patent office. It needs no construction. Come on. You can't rewrite the claim, and I think, Judge, that's about as easy to conclude as follows:

Now, before I get into repeated execution, which is the last thing, I would like to comment on a few of what my Brother said. Also, if we could pull up slide 19 of theirs, of Google's.

(A recess was taken.)

THE COURT: Mr. Hayes. And, again, if you need more time, I'll give it to you.

MR. HAYES: Are we going to 12?

THE COURT: Again, I've got a call with about 1,000 people starting at 12, so how about 11:50?

MR. HAYES: Okay, I can do that. Anyways, claim 19, my Brother puts that up as an analog signal. That's not an analog. They withdrew that. If you look at the patent and specifically column 14, lines 58 to I believe 60, 59 and 60, they're talking about an analog, typical analog signal itself modulates and then comes to stability over time period before it's even, before you even start going in and taking the

statistical mean thereof.

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It doesn't vary like this. If it varied like that and that thing to the right was a TV, nothing would work, your phone wouldn't work, nothing would work. If you look at the column 14, yeah, I bring it up now, but 59 and 60, they're talking about the variants of the signal approximately 1 percent. That's hardly 1 percent. That's just makeup to extend it.

Next my Brother uses -- you can take that off. Next my Brother uses -- you can take that off. Next, my Brother uses a slide, we'll just say, oh, this is heat. Who cares? If it's hot, you left your phone out for two days and it doesn't work, so what? We're talking about workable signals, et cetera, and so, I mean, that has nothing to do with the price of bread.

Let me see what else. If we look at slide 29, if you look at slide 29, they spend some time on that saying, oh, our expert confessed to something to that effect about fluctuation. That's cropped, Judge. Both of those two portions of the declaration are cropped.

In fact, if you read the next sentence on 33, it either goes along, it explains how it comes down to a stable situation that anybody who took second year calculus with a little statistics can figure out. The same thing here on the bottom line, 34, that's grossly cropped. The "..." is

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exactly opposite what they cited it for, and I point that out just simply because, you know, this is a consistent way, I can't go through every single thing that they cropped, but I think a few uncrops, a few cropped will sort of make my point.

Then someone said that, oh, there's no support in the spec. for the law of large numbers. Of course, there is. It says the idea is they get a statistical mean. A statistical mean by definition in a situation such as this where they are doing one billion executions a second is a statistical. I mean, the law of large numbers is exactly how you do it.

THE COURT: But that's the question, right, I mean, it gets back to repeated execution. Are we talking about a billion or a trillion or are we talking two or three?

MR. HAYES: No, we're not talking about any number, per se. You can't have a specific number. Why? Because all of the systems are different, all of the perturbations are different, and analog signals are going to have a different analog signal. The idea, if you do repeated executions, as we put in the declaration, pursuant to the law of large numbers, which, indeed, then flattens out exactly at a number, and that's exactly why if you look at the claim, you're not comparing an exact number to a number, a result. You're comparing to a statistical mean that is representative

of the population. That's the way it works.

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And to sort of put that to bed, let me just show you.

THE COURT: But what you're saying is that a person of ordinary skill in the art would read that and interpret it as such, right?

MR. HAYES: Absolutely, Judge, and, by the way, in my slides. Go to slide 25. Slide 25, all of these, what I just said there, are all supported in spades by our expert's declaration, and, interestingly, their expert, who they put their thing and doesn't even mention the words "statistical mean."

What he does is take the average, the average of 10 perturbations, right, and then divides, et cetera, to get an average. That is like saying this is a sytem that's operating at illiops a second. That's like saying I want to count everybody in China, and we're going to figure out how tall they are on average. We're going to take the first 10 guys off the street and a few women, and, boom, that's the answer.

And that is, as my expert said, not only absurd but is technically incompetent basically, and to do that, you've got the next one, 25.

Yes, 26, and this right there, I won't go through it, but that's the law of large numbers, et cetera, but

that's unrebutted, unrebutted, period.

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Next slide 26, same again, what a person of ordinary skill in the art would understand. Same again, unrebutted expert testimony as opposed to what do we have, attorney argument on one side, and the notion that they couldn't have submitted an expert report, sure, they could. All they had to do is ask leave to do it.

But, in any event, the next one, the next slide, this is, again, unrebutted, taking the average of a few samples, well, I guess Lee thinks that's okay.

And, anyways, if we look at slide, 28 okay. Now we get down to the nitty-gritty. The first thing that Google — the reason their argument is technically erroneous is they ignore the term "statistical mean." That's in the claim, it's not arithmetic average.

One skilled in the art would not think they're going to rely upon a number on that red box. Come on, do you know how long it takes to get that graph run where you get a flat statistical mean under the yellow box? That's less than one millionth of one second, period, and on the left, it is irrelevant. On the right is what one skilled in the art figures out. That's exactly how it's claimed.

You don't have to know that it's Number 12023 and put that in the claim. If that's the case maybe, what, one human alive would have traced, so to speak. That's not how

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you do statistics, and that's what they ignore. They ignore both of those, and as we can see, Judge, this happens here over and over, and the law of large numbers, that's like saying, oh, there's no support. Of course, there's support for the word "statistical," and we gave the definition of "statistical," et cetera. But that's like saying there's no support for a mechanical case, F equals MA, there's no support for gravity.

This has been around since I -- of course, it's been awhile -- went to engineering school, and our position is relatively simple. It's that one of ordinary skill in the art, who obviously would have taken, gone to college, would be an engineer, he would at least have taken a basic statistic course showing the law of large numbers, and he would be able to determine the statistical mean in one-millionth of a second, assuming, you know, assuming that he didn't skip that class.

But, in any event, then I want to give you this.

Judge, the reason I put this in is to just show you how one of ordinary skill in the art would determine infringement, that it's not indefinite, it can be done ASAP. Let's assume now, if you can see, the red blocks are just multipliers in the system. You got A and B going into the blocks. Out comes the first answer, let's assume it's an analog signal, as they say, and it's 4.1. The next one, you do the same

thing, and what is it, 4.3. The next one and the next one, and you get down to like A4, and it's C, 4.34, so you do that, and what you do is you do it pursuant to law of large numbers, if that's what you've been taught, and what do you get, you get a statistical mean at approximately 4.2. That's what you get.

Now, how do you know if it infringes or it doesn't infringe? Well, the first thing you do, according to the claim, is you multiply 2 times 2 because that's the exact mathematical input. You got the 4. Then you take the 4.2, which is the statistical mean, you subtract from it the 4, and you get .2, and then that is 5 percent, so that calculation is wrong. It's off by 5 percent.

All right. You know that. Then what you do is you repeat and do this. Is there another slide? Then what you do is you repeat this for all valid inputs, right, and then at the end of the day, if 5 percent of the executions are mathematically wrong by at least 5 percent, then you infringe. That is exactly how it's done. That's exactly how it's done in the complaint, by the way, et cetera, and this is it.

Anybody skilled in the art could do this without any concern, and now, Judge, I think with all due respect, hopefully you'll have time for lunch.

THE COURT: All right.

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1 MR. HAYES: Thank you.

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THE COURT: All right. Thank you. Do you want to do a quick response of any kind, I guess, Google, rather than naming any of the three lawyers, do you want to do a quick response?

MS. YBARRA: Yes, your Honor, I think I can do my response in about 10 minutes, if that's okay with, so you can have time for lunch.

THE COURT: All right.

MS. YBARRA: So, starting with a couple of points that Mr. Hayes made, he said Dr. Wei doesn't mention statistical mean in his declaration. That's not so. Dr. Wei discusses how the statistical mean overrepeated execution is indefinite, and Mr. Hayes said statistical mean, anybody would know what that means. That's not accurate either.

Dr. Khatri was asked multiple times in his deposition whether statistical mean differed from arithmetic mean in any way, and he refused to provide an answer and then said it was outside the scope of his opinion, and that's his testimony on that point is Exhibit A to our supplemental claim construction brief at page 54, line 6 through 25.

If we could pull up, Matthias, the last slide of our deck, Dr. Khatri's declaration at paragraph 34 that Mr. Hayes was just discussing. This is the graph as it's presented in Dr. Khatri's declaration, and he says the red box is meant to

zoom in on the graph below, and that graph plus the results of repeated execution of a multiplication operation, 2 times 1, and the blue line shows the average of the output value, and, Matthias, if we could do the animation there.

We were just kind of zooming in on Dr. Khatri's own figures here. I think there's one more animation there.

This is in Google's rebuttal slides.

MR. HAYES: There is no animation. You didn't send an animation to us, counsel.

THE COURT: Well, let's see it.

MS. YBARRA: All of the visuals are depicted on the slides that we sent you, but on the left side of the graph, the arithmetic average of the output is unstable and fluctuates significantly over short periods of time, as shown in the red box.

This is Dr. Khatri's language. This is from his declaration, and he says it begins to stabilize, the average begins to stabilize with more repeated executions of that single operation.

So as you move to the right, more time has past and more executions of the execution are repeated, and he says the magnified portion of the graph in the yellow box on the right represents the statistical mean over repeated execution were cited in the claims. So he says on the left side, that's not the stable statistical mean; on the right side, it

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is.

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But to be clear, it's not that the noise stabilizes, it's not that the noise flattens out, all Singular is saying is once you have a large enough number of samples, additional repeated executions are not going to impact the average, and that's just the law of large numbers, but the processor itself, the device itself is not stabilizing.

And every part of the blue line here reflects the statistical mean of a repeated execution, and that line drifts above and below the error threshold claimed by claim 1 of the '273 patent, which is the example we're using, and I think if you could do the last animation, Matthias. That is in this calculation of 2 times 1, the error threshold would be at 2.001.

And you can see there's a difference between the left and the right whether you are falling above or below the line. Dr. Khatri says ignore the left part of the line, ignore everything in the red box. He says it's only what's in the yellow box that's on the right side of the box that counts. Only this is the claimed statistical mean, and you just heard Mr. Hayes repeat the same thing that the left side of the graph is irrelevant, but that's just based on Dr. Khatri's opinion but nothing else. He can't point to --

THE COURT: I think, if I understand the argument right, he's saying it isn't just his opinion but this is what

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a person of skill in the art would understand that you don't look at the red box, you need to get to the yellow box. It's not his personal opinion, I think, I mean, I have to go back and look at it, I think that was the point that he's saying that a person of skill in the art would understand.

MS. YBARRA: He is saying a person of skill in the art would understand that, but that's not enough. He can't come in and make that statement without pointing to any objective guidance in the specification just say, and we cite case after case in our briefs for that very proposition.

It's not sufficient for him to say -- the specification has to give guidance to someone else looking at this to know what the scope of the claim is, and the blue line is the statistical, it represents the statistical mean of repeated execution all the way through, not just on the right side.

A different person of skill in the art might conclude that the statistical means stabilizes, or as Dr. Khatri refers to it, ceases to fluctuate significantly or doesn't meaningfully fluctuate at a different point than the yellow box on the right.

Someone else might conclude it happens towards the right side of the graph in the red box, which is flattening out there as well. Dr. Khatri says that this graph demonstrates the lack of a need to identify the precise number of executions necessary to ascertain the claims of

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statistical means, but actually it just shows the opposite:
How does one skilled in the art know at what point on the
graph the statistical mean has stabilized? That's our whole
point. There's nothing in the intrinsic evidence, looking at
the intrinsic evidence record that provides any guidance
about when you've reached stabilization. Is it, you know,
the right side of the red box? Is it the yellow box? Is it
somewhere in between because the line on Singular's graph, if
you look at Dr. Khatri's chart, is flat for most of the way
through.

One other point on this, and then I want to go to the last slide that Mr. Hayes had pulled up. Paragraph 36 of Dr. Khatri's declaration, he acknowledges that the line will not always flatten out in all circumstances. He implicitly concedes that heat is generating noise, the line won't flatten, the average won't stabilize, but he just dismisses that by saying, well, that device that produced such a result wouldn't be useful, which is not really helpful here.

If we could go to the last slide that Mr. Hayes was just looking at, and this was in Singular's deck, and I'll be brief here. First of all, this blue box here and the text in the blue box is incorrect insofar as it's tended to reflect the claim language. The blue box says it's 5 percent of all executions are mathematically wrong by .05 percent, the device infringes. That's not what the claims say. The

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claims say for at least 5 percent of the possible valid inference to the first operation, the statistical mean of a repeated execution must differ from the exact mathematical result by at least .05 percent.

really illustrates Google's point on indefiniteness.

Mr. Hayes says that he uses four different examples of an operation and then gets to end. An end here is doing all of the work. Whether or not the claim language is met will depend on N, and Singular shows you four calculations and then says after doing enough, you arrive at 4.2, which is presumably the stable statistical mean, but we saw with Dr. Khatri's testimony, he can't tell us how to figure out N, how many repeated executions does one need to perform.

There's no objective guidance anywhere in the patent record on that and none on this slide either. I think that is all from us --

THE COURT: Okay.

MS. YBARRA: -- unless you have any further questions.

THE COURT: All right. Mr. Hayes, last word.

MR. HAYES: Yes, your Honor. If you can pull up that slide that my Sister just referenced. She indicated all we did is take four numbers, et cetera. This is an example, obviously, A to the N does not -- it means it just keeps

going.

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Secondly, the blue box is not incorrect, it says repeat for all valid inputs, so that's a misstatement.

Third, when she keeps talking about Dr. Khatri and our expert, your Honor, I just suggest that the clerk read the quotes that she quotes because, believe it or not, 90 percent are indeed miscited.

Now, if you can give me the slide before that. Your Honor, I really believe that one of ordinary skill in the art would not, even Dr. Wei would not assume over there on the far left red box where it's bouncing around like that is a statistical mean of anything, and, frankly, if you look at what he did there, his calculations probably don't even get past the red line.

Don't forget, he did eight of them, and you can do 1500 one millionth of a second. I mean, I just think the argument that was made, now, from a factual point of view, his graph, as in the declaration of our expert, is exactly on the exact parameters given by Dr. Wei. The only difference is it complies with the law of large numbers and then generates a statistical mean as such. That's it, Judge, thank you.

THE COURT: All right. Thank you. We'll pause there. I know that sometimes after a Markman Hearing, the parties want to do follow-up, you know, supplemental

1 briefings either to correct or to respond to something. 2 will permit that, but let's get that in let's say close of 3 business a week from today, and I won't limit you. If you 4 think you have something to say, I'll let you say it. You 5 won't have to file anything. I won't put a page limit on it, 6 but remember the first rule, which is to have mercy on the 7 Judge and the law clerk, but I'll give you that opportunity 8 to respond if you want to take it or correct something, 9 supplement something, what have you. Thank you. This has 11:42AM 10 all been very helpful, well argued, and unless there's 11 anything further, we'll stand in recess. Thank you. 12 MR. BHANSALI: Thank you very much, your Honor. 13 MR. HAYES: Thank you, your Honor. 14 THE COURT: Thank you. 15 (Whereupon, the hearing was adjourned at 11:42 a.m.) 16 17 18 19 20 21 22 23 24 25

1	CERTIFICATE
2	
3	UNITED STATES DISTRICT COURT)
4	DISTRICT OF MASSACHUSETTS) ss.
5	CITY OF BOSTON)
6	
7	I do hereby certify that the foregoing transcript,
8	Pages 1 through 85 inclusive, was recorded by me
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11	and thereafter by me reduced to typewriting and is a true and
12	accurate record of the proceedings.
13	Dated April 3, 2021.
14	
15	s/s Valerie A. O'Hara
16	
17	VALERIE A. O'HARA
18	OFFICIAL COURT REPORTER
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